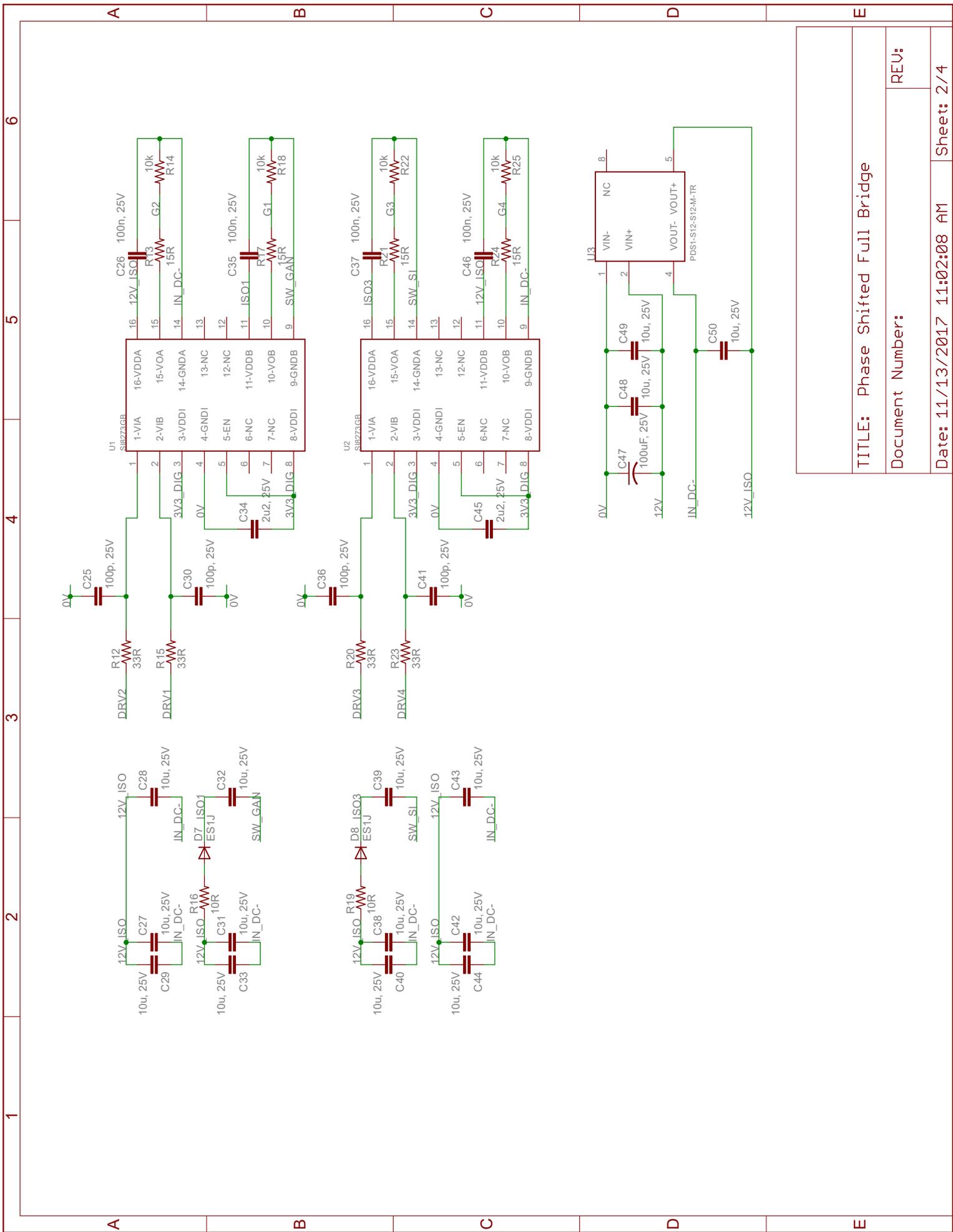


TITLE: Phase Shifted Full Bridge
 Document Number:

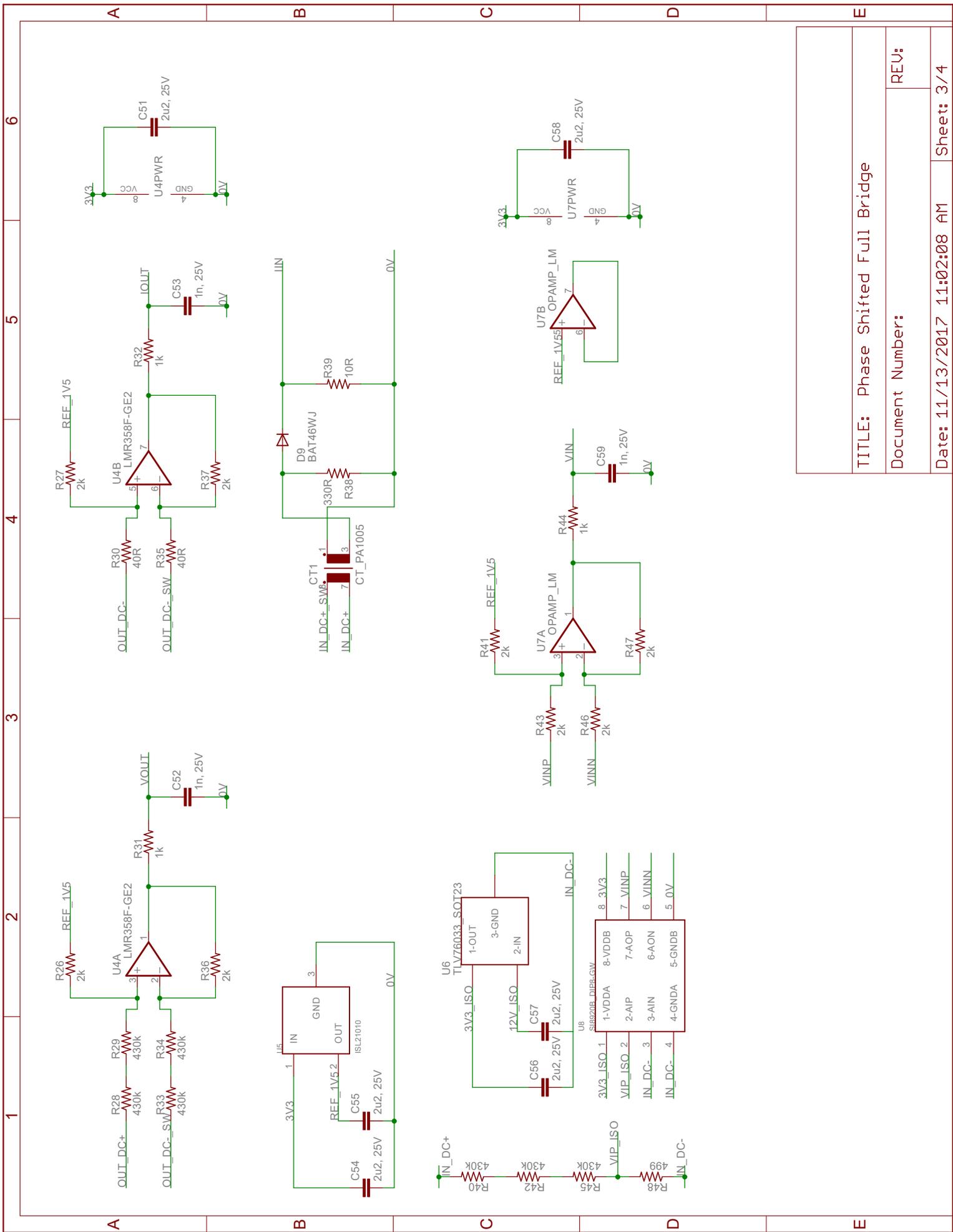
REV:

Date: 11/13/2017 11:02:08 AM

Sheet: 1/4



TITLE: Phase Shifted Full Bridge
 Document Number:
 Date: 11/13/2017 11:02:08 AM
 REV:
 Sheet: 2/4



6

5

4

3

2

1

A

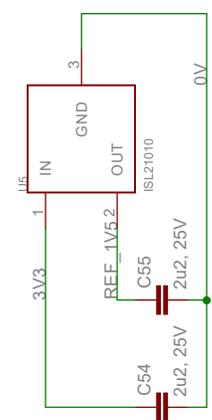
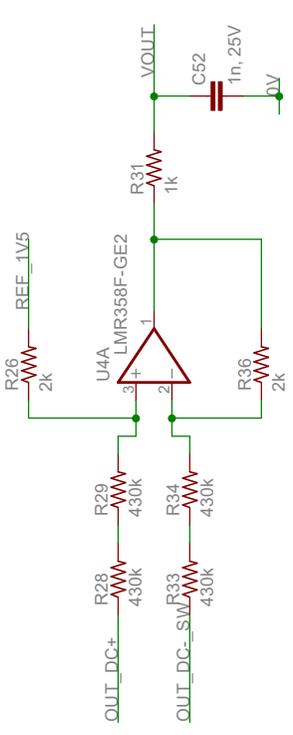
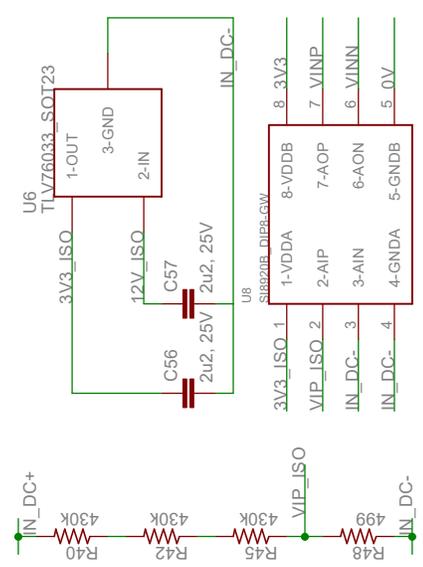
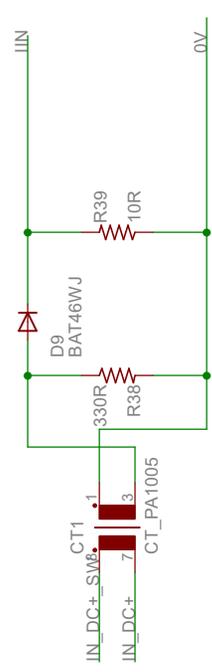
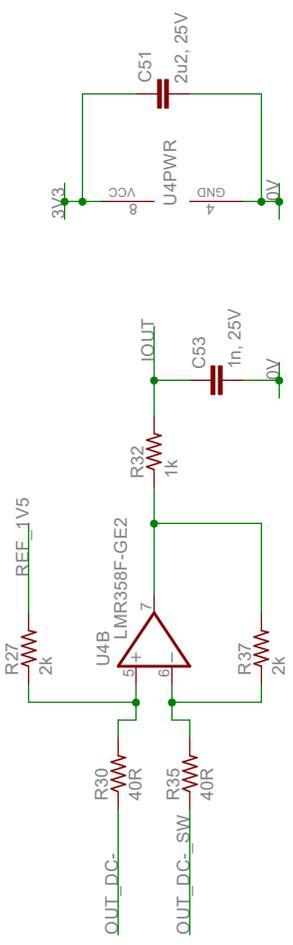
B

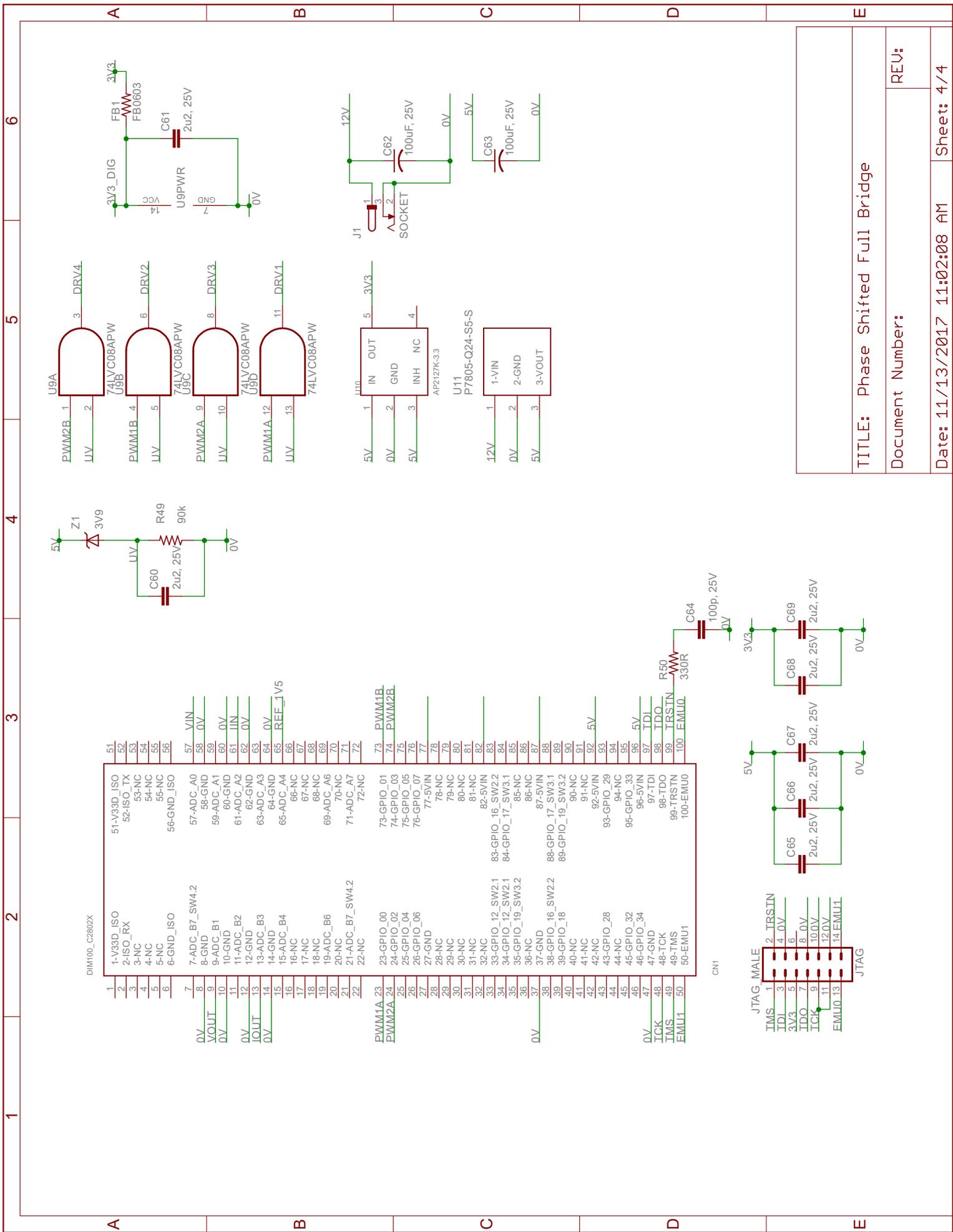
C

D

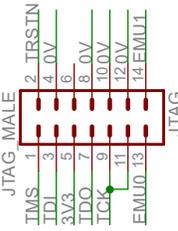
E

TITLE: Phase Shifted Full Bridge
 Document Number:
 Date: 11/13/2017 11:02:08 AM
 Sheet: 3/4





1	1-V33D_ISO	51	VIN
2	2-ISO_RX	52	0V
3	3-NC	53	0V
4	4-NC	54	0V
5	5-NC	55	0V
6	6-GND_ISO	56	0V
7	7-ADC_B7_SW4.2	57	0V
8	8-GND	58	0V
9	9-ADC_B1	59	0V
10	10-GND	60	0V
11	11-ADC_B2	61	0V
12	12-GND	62	0V
13	13-ADC_B3	63	0V
14	14-GND	64	0V
15	15-ADC_B4	65	REF_1V5
16	16-NC	66	0V
17	17-NC	67	0V
18	18-NC	68	0V
19	19-ADC_B6	69	0V
20	20-NC	70	0V
21	21-ADC_B7_SW4.2	71	0V
22	22-NC	72	0V
23	23-GPIO_00	73	PWM1B
24	24-GPIO_02	74	PWM2B
25	25-GPIO_04	75	0V
26	26-GPIO_06	76	0V
27	27-GND	77	0V
28	28-NC	78	0V
29	29-NC	79	0V
30	30-NC	80	0V
31	31-NC	81	0V
32	32-NC	82	0V
33	33-GPIO_12_SW2.1	83	0V
34	34-GPIO_12_SW2.1	84	0V
35	35-GPIO_19_SW3.2	85	0V
36	36-NC	86	0V
37	37-GND	87	0V
38	38-GPIO_16_SW2.2	88	0V
39	39-GPIO_18	89	0V
40	40-NC	90	0V
41	41-NC	91	0V
42	42-NC	92	5V
43	43-GPIO_28	93	0V
44	44-NC	94	0V
45	45-GPIO_32	95	0V
46	46-GPIO_34	96	0V
47	47-GND	97	0V
48	48-TCK	98	0V
49	49-TMS	99	0V
50	50-EMU1	100	0V



TITLE: Phase Shifted Full Bridge
 Document Number:
 REV:
 Date: 11/13/2017 11:02:08 AM
 Sheet: 4/4